Application No.: 09/941,602

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100. For example, the exemplary parallel anti-fuse programming circuit 100 illustrated in FIGS. 2A and 2B includes three of the anti-fuse programming circuits 12 shown in FIG. 1. the three anti-fuse programming circuits 12 share a common CGND signal line 16, so that all three anti-fuse devices 140, 141, 142 may be programmed substantially simultaneously with a single CGND programming pulse. The common CGND signal line 16 may be coupled to a common control input signal line 15 for this purpose.

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